

L Number	Hits	Search Text	DB	Time stamp
-	0	(716/4).ccls. and (master adj clock) and (evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 15:33
-	0	(716/5).ccls. and (master adj clock) and (evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 09:56
-	0	(716/6).ccls. and (master adj clock) and (evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 09:57
-	0	(716/\$).ccls. and (master adj clock) and (evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 09:58
-	1	(master adj clock) and (evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 10:03
-	146	(master adj clock) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 10:57
-	1	(master adj clock) and (asynchronous adj clock) and (evaluation adj time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 12:01
-	2	(asynchronous adj clock) and (evaluation adj time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 12:05
-	93	(evaluation) and (reconfigurable adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 13:05
-	5	(evaluation) same (reconfigurable adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 13:05
-	0	(reconfigurable adj logic) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 15:35
-	15	(reconfigurable adj logic) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 15:52

-	0	(reconfigurable adj logic) and ((asynchronous adj clock) adj phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 15:54
-	7	(reconfigurable adj logic) and ((asynchronous adj clock) same phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:03
-	1	((clock adj generation) adj slice) and ((asynchronous adj clock) same phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:15
-	1	((clock adj generation) adj slice) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:15
-	1	((clock adj generation) adj slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:31
-	165	((clock adj generation) adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:32
-	7	((clock adj generation) adj logic) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:32
-	1	((clock adj generation) adj logic) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 16:40
-	121	(clock adj generat\$) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 17:38
-	1	"4383314".PN.	USPAT	2003/07/15 16:49
-	121	(clock adj generat\$) same (asynchronous adj clocks)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 18:07
-	19	(clock adj generat\$) same (asynchronous adj clocks) and (trigger)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 18:46
-	4	(clock adj generat\$) same (asynchronous adj clocks) same (trigger)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 19:01

-	0	(clock adj generat\$) same (asynchronous adj clocks) same (trigger) and (toggle adj point)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/15 19:04
-	15	clock adj slice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:34
-	6771	clock and slice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:34
-	2206	(716/\$).ccls. and (clock asame slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:35
-	441	(716/4).ccls. and (clock asame slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:41
-	1	(716/4).ccls. and (clock same slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:44
-	0	(716/S).ccls. and (clock same slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:44
-	20	(716/\$).ccls. and (clock same slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/25 17:44
-	0	(716/4).ccls. and (maste adj clock) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 12:53
-	0	(716/4).ccls. and (master adj clock) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 12:58
-	0	(716/\$).ccls. and (master adj clock) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 12:58
-	149	(master adj clock) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 14:32

-	92	(FPGA) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 14:33
-	5	(evaluation adj logic) and (FPGA) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 14:48
-	13	(FPGA) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 15:19
-	3	(evaluation adj logic) same (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 15:21
-	126	(evaluation) same (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 15:33
-	89	(evaluation) same (FPGA) and (clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 15:46
-	24	(evaluation) same (FPGA) and (asy\$ adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 17:33
-	629044	(clock generation slice) and (trigger signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 17:36
-	2	((clock adj generation) adj slice) and (trigger adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 17:42
-	2	((clock adj generation) adj slice) and (trigger)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 17:45
-	25124	((clock) same (trigger))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 17:45
-	3308	(clock) same (trigger adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:18

-	9	(clock) same (trigger adj signal) same (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:27
-	0	(clock) same (trigger adj signal) same (clock adj scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:28
-	2	(clock) same (trigger adj signal) and (clock adj scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:29
-	45	(toggle adj point) and (trigger)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:30
-	0	(toggle adj point) same (trigger) same (clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:30
-	1	(toggle adj point) same (trigger) and (clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:31
-	24	(toggle adj point) and (trigger) and (clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:38
-	1832	clock same slice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:38
-	18	(clock adj generation) same slice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/11 18:39
-	250	(clock adj generation) and (slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 10:23
-	18	(clock adj generation) same (slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 10:31
-	2	(clock adj generation) adj (slice)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 10:32

-	158	trigger adj toggle	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 10:33
-	0	(trigger adj toggle) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 11:02
-	1	(trigger same toggle) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 11:40
-	2	(toggle adj point) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 11:45
-	2	(toggle adj point) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 11:46
-	127	(FPGA) same (evaluation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 16:30
-	46	((FPGA) same (evaluation)) and (asynchronous)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 16:39
-	2	((FPGA) same (evaluation)) and (asynchronous) and (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 16:43
-	31	((FPGA) same (evaluation)) and (asynchronous) and (phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 16:56
-	453	(clock adj generation) and (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 16:57
-	29	(clock adj generation) and (phase adj relationship) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 17:59
-	1	(verification adj system) and (reception adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:05

-	183	reception adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:21
-	3	(evaluation adj time) and (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:28
-	1247	(shift adj register) and (input adj data) and (verification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:30
-	20	(shift adj register) same (input adj data) same (verification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:31
-	1	(shift adj register) same (input adj data) same (verification) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:37
-	0	(input adj register) same (verification) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:38
-	7	(input adj logic) same (verification) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:49
-	16	clock adj slice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/12 18:50
-	0	(716/4).ccls. and (clock adj generation) and (clock adj schedule\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:04
-	0	(716/\$).ccls. and (clock adj generation) and (clock adj schedule\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:04
-	4	(clock adj generation) and (clock adj schedule\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:10
-	2	(clock adj generation) and (clock adj schedule\$) and (asynchronous)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:11

-	0	(FPGA) adj (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:13
-	5	(FPGA) same (asynchronous adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:30
-	9	(FPGA) same (reception adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:46
-	46	(asynchronous adj clock) and (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 10:49
-	10	(asynchronous adj clock) same (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 11:09
-	0	((asynchronous adj clock) adj (logic or unit)) same (phase adj relationship)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 11:10
-	2	((clock adj generation) adj logic) and ((asynchronous adj clock) same (phase adj relationship))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 11:11
-	2	((clock adj generation) adj (logic or unit)) and ((asynchronous adj clock) same (phase adj relationship))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 11:12